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ScholarResults 1 - 48 of 48 for **dsp integrated circuit "network processor"**. (0.02 seconds)[Addressing the system-on-a-chip interconnect woes through communication-based design - group of 29 »](#)

M Sgroi, M Sheets, A Mihal, K Keutzer, S Malik, J ... - PROC DES AUTOM CONF. pp. 667-672. 2001, 2001 - doi.ieeecomputersociety.org

... a reconfigurable **integrated circuit** for **DSP** applications that ... and methodologies to allow **integrated circuit** designers to ... Intel **Network Processor** Targets Routers ...

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[Hardware-Software Cosynthesis for Digital Systems - group of 9 »](#)

RK Gupta, G De Micheli - IEEE Design & Test of Computers, 1993 - mesl.ucsd.edu

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BE Boser, E Sackinger, J Bromley, Y leCun, LD ... - IEEE Micro, 1992 - ieeexplore.ieee.org

... are well matched to **integrated circuit** technology ... processor and **DSP** irriplenientation are very similar at 5.3 ... A special-purpose neural **network processor** must he ...

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[Network processors: a perspective on market requirements, processor architectures and embedded S/W ... - group of 12 »](#)

PG Paulin, F Karim, P Bromley - DATE, 2001 - doi.ieeeecs.org

... A **network processor** is a highly **integrated** set of ... technology has been applied to the MMDSP+ audio **DSP**. ... the instruction-set simulator and in-circuit emulator. ...

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[Parallel DSP testbed with a heterogeneous and reconfigurable network fabric - group of 7 »](#)

SK Tewksbury, K Devabattini, V Gandikota - PROC ANNU IEEE INT CONF INNOV SYST SILICON. pp. 310-322. ..., 1997 - msrc.wvu.edu

... Analog Devices SHARC DSPs), with a variety of parallel **DSP** array architectures ... of fine-grained processors **integrated** onto a single **integrated circuit** (ICs) as ...

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[An 80/20-MHz 160-mW Multimedia Processor **Integrated** With Embedded DRAM, MPEG-4 Accelerator, and 3-D ... - group of 6 »](#)

CW Yoon, R Woo, J Kook, SJ Lee, K Lee, HJ Yoo - IEEE JOURNAL OF SOLID STATE CIRCUITS, 2001 - ieeexplore.ieee.org

... frame buffers, and peripheral blocks are **integrated** together ... A MPEG-4 programmable codec **DSP** with an ... to network switches and high-speed **circuit** techniques, espe ...

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[The Challenge for Next Generation Network Processors. - group of 3 »](#)

I Agere - White Paper, 1999 - irwinlazar.com

... ASICs), Digital Signal Processors (**DSP**) or Reduced ... accounted for by the **network processor** at the ... Application Specific **Integrated Circuit** (ASIC) The application ...

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[Programmable Logic IP Cores in SoC Design: Opportunities and Challenges - group of 5 »](#)

SJE Wilton, R Saleh - PROCEEDINGS OF THE IEEE CUSTOM INTEGRATED CIRCUITS ..., 2001 - dspwork.com

... more thorough verification job would verify the **circuit** with a ... and as the complexities of **integrated** circuits increase ... of FPGA with IP Cores – **DSP** First Target ...

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Analog neuro-chip with on-chip learning capability for adaptive non-linear equalizers - group of 3 »

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... done on the chip and external DSP and Analog-to ... Sigmoid-derivative circuit, and an analog multiplier circuit. The numbers of integrated synapse and neuron cells ...

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N Shah - Master's thesis, University of California, Berkeley, Sep, 2001 - students.iit.ac.in

... them · Developers and designers in network processor related fields ... system

implementation: · ASIC (Application Specific Integrated Circuit) – any hardwired ...

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DR D'Mello, PG Gulak - Analog Integrated Circuits and Signal Processing, 1998 - Springer

... analog circuitry in one neural network processor [20] was ... field-programmable analog array integrated circuits that ... based on switched-capacitor circuit techniques ...

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J Guimaraes, A Romariz, P Ferreira, J Campelo, M ... - SBRN, 1998 - doi.ieeecs.org

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... Possible Network-Integrated Operating System Functions A. Idealized Model ... Circuit switched network link Store and forward, packet- switched ... D 4 DSP D 5 DSP D 6 ...

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Soldier Phone: An Innovative Approach to Wireless Multimedia Communications - group of 6 »

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... This protocol suite has been integrated with baseband ... 2. DSP and support logic providing preamble matched ... to support both packet and circuit switched traffic. ...

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U Ramacher - Proceedings of the IEEE International Joint Conference on ..., 1991 - ieeexplore.ieee.org

... have to be supported by a specially designed VLSI circuit which will ... of a sequence of elementary operations and their optional concatenations with DSP or host ...

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AD George, RB Fogarty, JS Markwell, MD Miars - SIMULATION-CALIFORNIA-, 1999 - hcs.ufl.edu

... to integrate software with microchip or circuit board simulation ... user's code is

integrated with a network ... Models In ISE, the network, processor, and software ...

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Design and optimization of networked wireless information systems - group of 4 »

M Srivastava - IEEE VLSI workshop, 1998 - ieeexplore.ieee.org

... higher than processor, memory and DSP cores, and ... Energy-efficient Radio Linked

Integrated Network) project ... power optimizations done during circuit, logic, and ...

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tapped MOS ... fibre amplifiers and equivalent electrical **circuit** 10 15 ...

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nonidealities are due to random variations in **integrated circuit** fabrication. ...

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Integrating high-level simulation into a model-integrated embedded system design toolset

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... VHDL – Very high speed **integrated circuit** Hardware Description Language ... principles of the model-integrated approach to ... TMS320C40 **DSP**, which is connected to a ...

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... If one considers an **integrated circuit** (IC) implementation ... COMPUTER-AIDED DESIGN OF **INTEGRATED** CIRCUITS AND ... mapped" into a programmable microprocessor or **DSP**. ...

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... DSP product (integrated from a DSP core and ... related to each other and the circuit has very ... Transactions on Computer-Aided Design of Integrated Circuits and ...

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... Several digital signal processing ICs such as the DSP- 56000 chip ... In the prototype neural network processor chip design, the synapse circuit in figure 6(a ...

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407 1045—9227/95\$04.00 © 1995 IEEE Implementing Regularly ...
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... 2.3.4 Technological and **circuit**-level conclusions ... this paradigm, the basic personal computing and communication device will be an **integrated**, battery-operated ...
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Digital Network ... PVC Permanent Virtual Circuit QoS Quality of Service ...
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